

What is claimed is:

1. A driving circuit of active matrix organic electroluminescence display is disclosed and a driving circuit consisting of one scan line and one data line on a display panel includes:
 - a scan reset TFT, a gate of the scan reset TFT connected to the scan line and a drain of the scan reset TFT connected to the data line;
 - a storage capacitor, having two ends installed between a source of the scan reset TFT and a supply line (Vdd);
 - a driving TFT, a source of the driving TFT connected to the supply line (Vdd);
 - a detect TFT, a gate of the detect TFT connected to a Threshold-Lock, a drain of the detect TFT connected to the gate of the driving TFT and a source of the detect TFT connected to a drain of driving TFT;
 - a compensation capacitor, having two ends installed between source of the scan reset TFT and drain of the detect TFT;
 - an organic electroluminescence element, the anode of the organic electroluminescence element connected to the drain of the driving TFT and cathode connected to a common line;
 - a switch on the display panel is used to connect the

common line and the grounding end.

2. The driving circuit of active matrix organic electroluminescence display according to claim 1, wherein the detect TFT of each pixel circuit on a display
5 substrate is controlled by the Threshold-Lock.

3. The driving circuit of active matrix organic electroluminescence display according to claim 1, wherein the source of driving TFT in every pixel circuit on a display substrate is jointly connected to a supply
10 line.

4. The driving circuit of active matrix organic electroluminescence display according to claim 1, wherein the cathode of organic electroluminescence element in every pixel circuit on a display substrate is
15 jointly connected to a common line.

5. The driving circuit of active matrix organic electroluminescence display according to claim 1, wherein the switch is a thin film transistor (TFT).

6. The driving circuit of active matrix organic
20 electroluminescence display according to claim 1, wherein the switch is controlled by a display line.

7. The driving circuit of active matrix organic electroluminescence display according to claim 1, wherein a cycle of driving signals can be divided into
25 three phases: Threshold-Lock Phase, write phase

and display phase.

8. The driving circuit of active matrix organic electroluminescence display according to claim 7, wherein voltage level of the data line is reset to be
5 the same as that of supply line (Vdd) in the beginning of Threshold-Lock Phase.

9. The driving circuit of active matrix organic electroluminescence display according to claim 7, wherein the storage capacitor discharges and resets in
10 Threshold-Lock Phase and the compensation capacitor memorizes the threshold voltage (V_{th}) of driving TFT.

10. The driving circuit of active matrix organic electroluminescence display according to claim 7,
15 wherein data voltage in write phase will be stored in the storage capacitor as scan reset TFT is on and threshold voltage (V_{th}) previously memorized by the compensation capacitor will still be retained as detect TFT is off.

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